

Title (en)
PROGRAMMABLE PACER WITH VARIABLE AMPLIFIER GAIN

Publication
EP 0000984 B1 19830511 (EN)

Application
EP 78300237 A 19780803

Priority
• GB 3491777 A 19770819
• US 91714278 A 19780619

Abstract (en)
[origin: EP0000984A1] An implanted receiver-decoder stores program bits corresponding to rate alteration and to input amplifier sensitivity. The former bits operate a rate decoder (4) which in turn is responsive to predetermined outputs of a fixed rate oscillatorcounter (1, 2). The input amplifier (12) is connected to the intermediate tap of a potential divider (11, 14, 15), one arm (11) of which senses signals at the heart, and the other of which is either set at a fixed potential, or is allowed to float, under control of the input amplifier sensitivity programming signal.

IPC 1-7
A61N 1/36

IPC 8 full level
A61N 1/372 (2006.01)

CPC (source: EP)
A61N 1/37211 (2013.01)

Cited by
EP0491907A4; EP0011932A3; FR2440199A1; US11944526B2

Designated contracting state (EPC)
DE FR GB NL SE

DOCDB simple family (publication)
EP 0000984 A1 19790307; EP 0000984 B1 19830511; AU 3895778 A 19800221; CA 1098591 A 19810331; DE 2862246 D1 19830616

DOCDB simple family (application)
EP 78300237 A 19780803; AU 3895778 A 19780816; CA 309563 A 19780817; DE 2862246 T 19780803