

Title (en)

METHOD OF MANUFACTURING A CHANNEL PLATE STRUCTURE

Publication

EP 0006267 B1 19821110 (EN)

Application

EP 79200291 A 19790611

Priority

GB 7826877 A 19780614

Abstract (en)

[origin: EP0006267A1] An electron multiplier using a laminated channel plate assembly. The electron multiplier is usable in display tubes and image intensifiers. A problem in channel plate electron multipliers is to space apart accurately the dynodes in a simple and inexpensive way. In the present invention this problem is overcome by using discrete separating elements (18) such as ballotini to space apart the dynodes (11, 12, 13, 14). The elements (16) are bonded to the surface of one dynode of adjacent pairs of dynodes and is either bonded to or clamped against the other dynode of the pair. Various methods of making the laminated channel plate assembly are disclosed.

IPC 1-7

H01J 31/50; H01J 43/10; H01J 43/18

IPC 8 full level

H01J 43/24 (2006.01); **H01J 1/32** (2006.01); **H01J 1/88** (2006.01); **H01J 9/12** (2006.01); **H01J 43/22** (2006.01)

CPC (source: EP)

H01J 1/88 (2013.01); **H01J 43/22** (2013.01)

Cited by

EP0151502A1; EP2124240A1; US4737623A; EP1560254A3; FR2608316A1; EP1011125A4; WO0175933A1; US6841935B2; EP1560254A2; US6998778B2; US7042155B2; US6380674B1

Designated contracting state (EPC)

DE FR GB IT

DOCDB simple family (publication)

EP 0006267 A1 19800109; EP 0006267 B1 19821110; CA 1139821 A 19830118; DE 2964009 D1 19821216; GB 2023332 A 19791228; GB 2023332 B 19821027; JP S5516392 A 19800205; JP S6141097 B2 19860912

DOCDB simple family (application)

EP 79200291 A 19790611; CA 329423 A 19790607; DE 2964009 T 19790611; GB 7826877 A 19780614; JP 7515779 A 19790614