Title (en)

DIGITAL SIGNAL TRANSMISSION SYSTEM

Publication

EP 0016336 B1 19830810 (EN)

Application

EP 80100765 A 19800214

Priority

JP 1734179 A 19790219

Abstract (en)

[origin: US4330856A] In a digital signal transmission system wherein input digital signals having an arbitrary bit rate are converted into digital signals of a bit rate higher than that of the input digital signals and then transmitted through a signal format converter; in order to realize the signal format conversion in real time the signal format converter is constructed of a buffer circuit of small capacity, a circuit which writes the input signals into the buffer circuit at the bit rate of said input signals and which reads out the written signals at the bit rate of a transmission line, a circuit which distinguishes the signals to-be-read as a mark, space and empty, and an encoding circuit which converts the mark, space and empty into pulse signals discernible with the unit being a time slot of the transmission line or integral times the time slot, in dependence of the levels of pulses or the numbers of successive pulses (run length).

IPC 1-7

H04J 3/07; H04L 25/36; H04J 7/00

IPC 8 full level

H04L 25/38 (2006.01); **H04J 3/06** (2006.01); **H04J 3/07** (2006.01); **H04J 7/00** (2006.01); **H04L 7/00** (2006.01); **H04L 25/05** (2006.01); **H04L 25/49** (2006.01)

CPC (source: EP US)

H04J 3/07 (2013.01 - EP US); **H04J 7/00** (2013.01 - EP US); **H04L 25/05** (2013.01 - EP US)

Cited by

US4525835A; EP0219016A3; AU572533B2

Designated contracting state (EPC)

DE FR GB NL

DOCDB simple family (publication)

US 4330856 A 19820518; CA 1123963 A 19820518; DE 3064511 D1 19830915; EP 0016336 A1 19801001; EP 0016336 B1 19830810; JP S55110448 A 19800825; JP S5923660 B2 19840604

DOCDB simple family (application)

US 11891980 A 19800206; CA 345980 A 19800219; DE 3064511 T 19800214; EP 80100765 A 19800214; JP 1734179 A 19790219