

Title (en)
SEGMENTED-DISPLAY DEVICE

Publication
EP 0020980 A3 19820317 (FR)

Application
EP 80102574 A 19800509

Priority
US 5205479 A 19790625

Abstract (en)
[origin: US4237458A] A stroke expansion technique for use in controlling incremental stroke displays. The technique results in a substantial reduction of the stroke memory required for storage of the stroke sequences of an alphanumeric character set. Logic circuitry is operable to recognize a stroke of the same video state (on or off) in the reverse direction of the immediately preceding stroke to cause a predetermined number of additional strokes identical to the immediately preceding stroke to be generated instead of utilizing the reverse stroke to return the beam to its position immediately previous to execution of the preceding stroke.

IPC 1-7
G09G 1/10

IPC 8 full level
G09G 1/00 (2006.01); **G09G 1/08** (2006.01); **G09G 1/10** (2006.01); **G09G 5/22** (2006.01); **G09G 5/24** (2006.01)

CPC (source: EP US)
G09G 1/10 (2013.01 - EP US)

Citation (search report)
• [A] US 3755805 A 19730828 - DANDREL P, et al
• [A] US 3459926 A 19690805 - HEILWEIL MELVIN F, et al
• [A] IBM TECHNICAL DISCLOSURE BULLETIN, vol. 15, no. 9, février 1973, New York, (US) P.F. NEE: "Compaction of Vector Data for Display Devices", page 2966

Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
EP 0020980 A2 19810107; EP 0020980 A3 19820317; EP 0020980 B1 19850403; CA 1135427 A 19821109; DE 3070411 D1 19850509; JP S5638092 A 19810413; JP S623955 B2 19870128; US 4237458 A 19801202

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EP 80102574 A 19800509; CA 349793 A 19800414; DE 3070411 T 19800509; JP 4581480 A 19800409; US 5205479 A 19790625