

Title (en)
TRANSISTOR CONVERTER CIRCUIT

Publication
EP 0023263 B1 19830112 (DE)

Application
EP 80103465 A 19800620

Priority
DE 2927837 A 19790710

Abstract (en)
[origin: EP0023263A1] 1. Transistor inverted converter circuit comprising a transformer (11; 111) having a primary winding (11a; 111a) which can be connected to a d.c. voltage source (1; 101) in series with the emitter-collector path of a transistor (4, 5; 105), a secondary winding (11b; 111b) to which a load (15; 115) can be connected, and a control winding (6, 162) which is connected by one terminal to the base of the transistor (4, 5; 105) and which is connected by another terminal to the emitter of the transistor (4, 5; 105) by way of a diode means (base-emitter path of 5 or 4; diode 142), and which is provided between the two terminals with a tapping (6d; c) which is connected to a terminal of a biasing capacitor (9, 109), the other terminal of which is connected by way of a resistor (10; 110) to the emitter of the transistor (4, 5; 105), further comprising a commutating capacitor (12; 112) which is part of an oscillator circuit determining the frequency of the converter circuit, and a charging circuit having a diode (7, 8; 107) for charging the biasing capacitor (9, 109) to a d.c. control voltage which is of a polarity for conduction by the transistor (4, 5; 105), characterised in that the primary winding of the transformer (11, 111) is connected to the d.c. voltage input by way of a choke (2; 102), that the charging circuit is a closed circuit which is formed by the diode (7, 8; 107) serving as rectifier means, the biasing capacitor (9, 109) and at least a part (6a-6d, 6b-d; a-c) of the control winding (6; 162), and that the resistor (10; 111) which is connected to the biasing capacitor (9; 109) is only part of the control circuit (Figure 1; Figure 3).

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H02M 7/537; **H05B 41/29**

IPC 8 full level
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