

Title (en)

TIME-KEEPER IN PARTICULAR QUARTZ CONTROLLED CLOCK

Publication

EP 0023490 B1 19830720 (DE)

Application

EP 79901536 A 19800603

Priority

- DE 2850295 A 19781120
- DE 2850325 A 19781120
- DE 2850357 A 19781120

Abstract (en)

[origin: WO8001113A1] The clock comprises an analogue display, a quartz oscillator (1) and electronic frequency dividers (6, 7). The indicator elements are driven by a motor (SM) controlled electronically and comprising a working winding (87) and control winding (68). The control and setting circuit of the motor (SM) is provided so as to allow the adjustment of the rotation speed only at a speed higher than the rated rotation speed and, upon a frequency variation of the clock, to add correction signals to the control pulses of the working winding (87), said pulses having the shape of an alternating current, through a bistable flip-flop (50), to which are applied the pulses derived from the frequency dividers (6, 7) and the pulses derived from the motor (SM), a gate NO-AND (58) connected to an output of the flip-flop (50) and to another frequency divider (50), a field effect transistor (88) and a resistance (89). These correction signals increase the amplitude of the operating electrical signal proper to the working winding (87) and accelerate the motor (SM) at the set maximum rotation speed.

IPC 1-7

G04C 3/14

IPC 8 full level

G04C 3/14 (2006.01)

CPC (source: EP US)

G04C 3/14 (2013.01 - EP US)

Designated contracting state (EPC)

CH FR GB

DOCDB simple family (publication)

WO 8001113 A1 19800529; EP 0023490 A1 19810211; EP 0023490 B1 19830720; JP S55501033 A 19801127; US 4417820 A 19831129

DOCDB simple family (application)

DE 7900137 W 19791115; EP 79901536 A 19800603; JP 50200379 A 19791115; US 35893482 A 19820317