

Title (en)
CIRCUIT ARRANGEMENT FOR INTRUSION OR FIRE ALARM SYSTEMS

Publication
EP 0026461 A3 19810513 (DE)

Application
EP 80105763 A 19800924

Priority
DE 2939494 A 19790928

Abstract (en)
[origin: EP0026461A2] 1. A circuit arrangement for burglar and fire alarm systems for monitoring individual alarm lines (L1, L2, ...) in respect of alarm sabotage and disturbance in accordance with the rest current principle, having a current measuring device (MI) which is arranged in a central unit (Z) in the alarm line (L1) in order to determine the line resistance and whose output is connected to a plurality of comparators (K1, K2, K3), and having alarm units (M1, M2, ...) which respectively possess an alarm contact (Ka) and a sabotage contact (KS) which are connected to one and the same line (L1), characterised in that a plurality of alarm units (M1, M2, ...) are connected to one line (L1), that in the individual alarm units (M1, M2, ...), there is arranged a first resistor (RS) in series with the sabotage contact (KS) in line (L1) and the series connection of a second resistor (RA) and the alarm contact (KA) is arranged parallel to the first resistor (RS), where the resistance values are so dimensioned that the response of an alarm contact (KA) or the simultaneous response of the alarm contacts of a plurality of alarm units effects a line resistance change which is distinguishable from the response of a sabotage contact (KS) and which indicates a separate alarm and sabotage call (A, S) which are independent of one another.

IPC 1-7
G08B 29/00; **G08B 13/22**

IPC 8 full level
G08B 13/22 (2006.01); **G08B 29/00** (2006.01); **G08B 29/08** (2006.01)

CPC (source: EP)
G08B 13/22 (2013.01); **G08B 29/08** (2013.01)

Citation (search report)
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• US 4198625 A 19800415 - MANDE IRVING [US], et al
• ELEKTOR, Band 2, Nr. 1, Januar 1976, Elektor Publishers Ltd, Canterbury, GB, "High security burglar alarm sensor", Seite 149

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Designated contracting state (EPC)
AT BE CH FR GB IT NL SE

DOCDB simple family (publication)
EP 0026461 A2 19810408; **EP 0026461 A3 19810513**; **EP 0026461 B1 19831019**; AT E5108 T1 19831115; DE 2939494 A1 19810402; DE 2939494 B2 19810702

DOCDB simple family (application)
EP 80105763 A 19800924; AT 80105763 T 19800924; DE 2939494 A 19790928