

Title (en)
PHASE LOCKED LOOP CONTROL SYSTEM.

Title (de)
PHASENSTARRES SCHLEIFENSTEUERSYSTEM.

Title (fr)
SYSTEME DE COMMANDE D'UNE BOUCLE FERMEE DE PHASE.

Publication
EP 0026785 A4 19820614 (FR)

Application
EP 80900859 A 19801023

Priority
US 2870279 A 19790410

Abstract (en)
[origin: US4272712A] In the disclosed system, if the frequency of input signals exceeds a value suitable for a load, the input signals and output signals are stored and compared in a comparator while locking of a phase locked loop is delayed by a loop filter that allows the output of the VCO (voltage controlled oscillator) to increase or decrease only at rates slower than given rates. When the input signals end after the loop locks, the filter delay causes the VCO to keep producing signals which are passed until the comparator recognizes that the number of output signals equals the number of input signals. If the input signals end before the loop locks, a logic circuit causes the VCO frequency to keep rising until another comparator indicates that the number of output signals has reached one-half the input signals.

IPC 1-7
G05B 19/40; **G05B 13/00**; **G05B 1/02**; **H02P 7/00**

IPC 8 full level
G05B 19/40 (2006.01)

CPC (source: EP US)
G05B 19/40 (2013.01 - EP US); **Y10S 388/911** (2013.01 - EP); **Y10S 388/912** (2013.01 - EP)

Citation (search report)

- US 3553549 A 19710105 - LEENHOUTS ALBERT C
- GB 1404774 A 19750903 - ELEKTROMAT VEB
- See references of WO 8002204A2

Designated contracting state (EPC)
FR

DOCDB simple family (publication)
WO 8002204 A2 19801016; **WO 8002204 A3 19801113**; EP 0026785 A1 19810415; EP 0026785 A4 19820614; GB 2059112 A 19810415; GB 2059112 B 19830615; US 4272712 A 19810609

DOCDB simple family (application)
US 8000383 W 19800331; EP 80900859 A 19801023; GB 8039103 A 19800331; US 2870279 A 19790410