

Title (en)  
TESTOR FOR MICROPROCESSOR-BASED SYSTEMS.

Title (de)  
TESTGERÄT FÜR AUF MIKRORECHNER BASIERTE SYSTEME.

Title (fr)  
APPAREIL D'ESSAI POUR SYSTEMES A BASE DE MICROPROCESSEURS.

Publication  
**EP 0032895 A1 19810805 (EN)**

Application  
**EP 79901509 A 19810224**

Priority  
US 7900550 W 19790727

Abstract (en)  
[origin: WO8100475A1] An automatic test apparatus (10) coupled to a bi-directional internal data and control bus (26) of a programmed microprocessor-based system (14) tests the performance of the system (14) in real time. Signal responses of the bus (26) within a known good system (12) are compared in real time to signal responses on the bus (26) of the unknown system under test (14) in order to identify faults. Faults are isolated by a sequential algorithm which stops operation at the designated occurrence of a fault, typically the first fault, and thereupon traces the error causing the fault to the subsystem or components in the system under test (14) supplying the indicated error signal to the bi-directional bus (26).

Abstract (fr)  
Un appareil d'essai automatique (10) couple a un bus de commande et de donnee internes bidirectionnelles (26) d'un systeme a base de microprocesseurs programme (14) controle le rendement du systeme (14) en temps reel. Des reponses de signaux du bus (26) dans un bon systeme connu (12) sont comparees en temps reel aux reponses de signaux sur le bus (26) du systeme non connu a l'essai (14) de maniere a identifier les defaillances. Les defaillances sont isolees par un algorithme sequentiel qui arrete le fonctionnement au moment designe ou se produit une defaillance, generalement la premiere defaillance, puis il etablit quel est le sous-systeme ou composant defectueux du systeme a l'essai (14), envoyant le signal d'erreur indiquee au bus bidirectionnel (26).

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