

Title (en)
PATTERN POSITION RECOGNITION APPARATUS

Publication
EP 0041870 B1 19861230 (EN)

Application
EP 81302575 A 19810610

Priority
• JP 7823580 A 19800610
• JP 18725880 A 19801229

Abstract (en)
[origin: EP0041870A1] The apparatus has an image pickup section 3 for picking up an image of a pad pattern on a IC chip 8. Image signals from the image pickup section 3 are binary-coded in a signal processing system 4. The binary-coded signals are subjected to a first slice level, to produce an image of the IC chip as shown in Figure 2C. The position of the IC chip 8 is detected by a chip position detecting system 5, and in dependence upon the detected position of the chip 8 the position of a view window 11 is determined by view window determining circuit 6. A pad pattern to be identified is caught in the view window 11 and image signals representing the image in the view window 11 are binary coded by the signal processing system 4, but are subjected to a different slice level. On the basis of the binary-coded and slice signals, pad pattern identifying system 7 recognises the position of the pad pattern in the view window 11.

IPC 1-7
G03B 41/00

IPC 8 full level
H01L 21/68 (2006.01)

CPC (source: EP US)
H01L 21/681 (2013.01 - EP US)

Cited by
CN102487529A; US4855928A; GB2184233A; GB2139348A; GB2146427A; US4719357A; GB2212961A; US4791586A; EP0106534A3

Designated contracting state (EPC)
DE FR GB NL

DOCDB simple family (publication)
EP 0041870 A1 19811216; EP 0041870 B1 19861230; DE 3175773 D1 19870205; US 4450579 A 19840522

DOCDB simple family (application)
EP 81302575 A 19810610; DE 3175773 T 19810610; US 27236881 A 19810610