

Title (en)  
MONOLITHIC INTEGRATED CIRCUIT AND APPLICATION TO A PACEMAKER

Publication  
**EP 0043957 B1 19830907 (DE)**

Application  
**EP 81104931 A 19810625**

Priority  
DE 3026233 A 19800710

Abstract (en)  
[origin: EP0043957A1] 1. A monolithic integrated circuit, in which components are formed in a doped substrate by differently doped regions, and in which the substrate is firmly connected to that pole of a supply voltage source which causes the junction between the component and the substrate to be in the blocking state, characterized in that on the occurrence of a voltage external to the supply voltage such that the junction would become conductive, the substrate (9) is connected approximately to this voltage.

IPC 1-7  
**G05F 3/20; A61N 1/36**

IPC 8 full level  
**A61N 1/36** (2006.01); **G05F 3/20** (2006.01)

CPC (source: EP)  
**G05F 3/205** (2013.01)

Cited by  
CN107769742A

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DOCDB simple family (publication)  
**EP 0043957 A1 19820120; EP 0043957 B1 19830907**; AT E4615 T1 19830915; AU 7270581 A 19820114; DE 3026233 A1 19820211

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**EP 81104931 A 19810625**; AT 81104931 T 19810625; AU 7270581 A 19810709; DE 3026233 A 19800710