

Title (en)

Plasma display panel drive.

Title (de)

Plasmaanzeigesteuereinrichtung.

Title (fr)

Dispositif de commande d'un panneau d'affichage à plasma.

Publication

EP 0044182 A2 19820120 (EN)

Application

EP 81303065 A 19810706

Priority

- US 16657980 A 19800707
- US 25875781 A 19810429
- US 27288581 A 19810612

Abstract (en)

A circuit for performing sustaining and pulsing operations for an AC plasma panel is disclosed, wherein two MOSFET transistors are used to alternatively, selectively provide a two level output signal. The circuit requires only a single logic input, thus preventing both transistors from conducting simultaneously, and features full slew rate control of the output. The drive circuitry is improved so that the waveforms applied to the panel electrodes to perform the sustain, write, erase, and bulk-erase functions are simplified to comprise a sequential series of rectangular waves, and only one high voltage power supply is required. High voltage is supplied to the driver chips only when they are to perform a write or erase function, thus greatly reducing the amount of power which must be dissipated in the driver chips.

IPC 1-7

G09G 3/28; H03K 17/66

IPC 8 full level

G09G 3/28 (2006.01); **G09G 3/288** (2006.01); **G09G 3/296** (2013.01)

CPC (source: EP)

G09G 3/296 (2013.01); **G09G 2310/066** (2013.01); **G09G 2330/021** (2013.01)

Cited by

WO0241292A1; EP0106942A3; US5909199A; EP0261584A3; EP0337833A1; FR2629245A1; EP0715292A3; US5696522A; FR2816746A1; EP0102445A3; US7138994B2

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