

Title (en)

Circuit for correcting error in digital information signal.

Title (de)

Fehler-Korrekturschaltung für ein digitales Informationssignal.

Title (fr)

Circuit pour corriger les erreurs dans un signal d'information numérique.

Publication

EP 0048933 A1 19820407 (EN)

Application

EP 81107526 A 19810922

Priority

JP 13287480 A 19800926

Abstract (en)

[origin: US4408326A] An error correcting circuit particularly for an audio equipment makes use of the so-called adjacent code correction technology and an error pointer generated by the error detection code (CRCC). The circuit comprises a counter starting to count clock pulses in response to an operation start signal, a timing signal generator for steps of correction operation, a correction operation circuit operative with the timing signal, a storage circuit for storing operation results of the correction operation circuit, a monitoring circuit for monitoring the degree of execution of the correction operation steps in the correction operation circuit, using the output of the counter and a control circuit for controlling the storage of the operation results in the storage circuit, depending upon the output of the monitoring circuit.

IPC 1-7

G11B 5/09

IPC 8 full level

G11B 20/18 (2006.01); **H03M 13/00** (2006.01); **H03M 13/09** (2006.01)

CPC (source: EP US)

G11B 20/1809 (2013.01 - EP US); **H03M 13/09** (2013.01 - EP US)

Citation (search report)

- DE 2948126 A1 19800626 - MATSUSHITA ELECTRIC IND CO LTD
- GB 2038514 A 19800723 - MATSUSHITA ELECTRIC IND CO LTD
- FR 2443171 A1 19800627 - MATSUSHITA ELECTRIC IND CO LTD [JP]
- US 4211997 A 19800708 - HEASLETT ALASTAIR M [US], et al
- US 4145683 A 19790320 - BROOKHART MARSHALL R
- GB 2042228 A 19800917 - TOKYO SHIBAURA ELECTRIC CO
- US 3622984 A 19711123 - EASTMAN HAL P
- Conference Record 1978, National Telecommunications Conference, December 3-6, 1978, Vol. 2, Birmingham; Alabama, US INOUE et al. "A New Class of Burst-Error-correcting codes and its Application to PCM tape Recording Systems", pages 20.6.1-20.6.5 * figure 5, from page 20.6.3, right-hand column, line 8 to page 20.6.5, left-hand column line 15 *
- British Kinematography Sound and Television Society Journal, Vol. 62, No. 11, November 1980 London, GB YOKOYAMA et al. "An Experimental Digital Videotape Recorder", pages 570-580.

Cited by

EP0223255A3; EP0152702A3; EP0094671A3; FR2514925A1; US4533964A; EP0093969A3; CN110663207A

Designated contracting state (EPC)

DE GB

DOCDB simple family (publication)

US 4408326 A 19831004; DE 3173715 D1 19860320; EP 0048933 A1 19820407; EP 0048933 B1 19860205; JP H0320832 B2 19910320; JP S5758210 A 19820407

DOCDB simple family (application)

US 30537581 A 19810924; DE 3173715 T 19810922; EP 81107526 A 19810922; JP 13287480 A 19800926