

Title (en)
Memory device.

Title (de)
Speicheranordnung.

Title (fr)
Dispositif de mémoire.

Publication
EP 0049462 A2 19820414 (EN)

Application
EP 81107735 A 19810929

Priority
JP 13848780 A 19801003

Abstract (en)
A memory device, which comprises a capacitor (20) connected to a power supply terminal (10) through a main switch (12) to be used as a data holding power source and also connected to a read write memory (24). The memory device further comprises a comparator (22) which compares the level of the terminal voltage of the capacitor (20) with a minimum level of the data holding voltage of the memory (24) and a transistor (16) which is connected between the main switch (12) and capacitor (20) and rendered conducting in accordance with an output signal from the comparator (22).

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G11C 11/00

IPC 8 full level
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CPC (source: EP)
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Cited by
EP0411201A1; EP0440204A3; EP0950943A1; GB2203005A; GB2203005B; EP0213577A3; EP0171418A4; EP0421839A1; FR2652672A1; US5177707A; US6252312B1; US7421603B2; WO03060922A3

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