Title (en)
SPEECH PROCESSOR

Publication
EP 0051462 A3 19820609 (EN)

Application
EP 81305149 A 19811029

Priority
US 20304280 A 19801103

Abstract (en)
[origin: EP0051462A2] A system and apparatus for generating human speech or other complex sounds from a digital command signal and using a hybrid synthesis technique comprises a digital, fixed repertoire speech processor (10) which includes a first circuit (12) for modeling the behaviour of the human vocal tract and a second circuit (11) for controlling the behaviour of the modeling circuit (12). The processor may advantageously be implemented on a single VLSI integrated circuit chip.

IPC 1-7
G10L 1/08

IPC 8 full level
G10L 19/06 (2006.01); G10L 11/00 (2006.01); G10L 19/00 (2006.01)

CPC (source: EP)
G10L 25/00 (2013.01)

Citation (search report)
- EP 0016427 A2 19801001 - CSELT CENTRO STUDI LAB TELECOM [IT]
- Electronics International, Vol. 53, No. 3, January 31, 1980 New York (US) M.E. HOFF et al.: "Software makes a Big Talker out of the 2920 Microcomputer" pages 102-107 * figures 3,4 *
- Nachrichten Electronik, Vol. 33, No. 12, December 1979 Heidelberg (DE) "Programmierbarer Digital-Signalprozessor fur Sprachysnthese" pages 399,400 * page 399, center column, lines 16-23; figure 1 *
- ICASSP 80, Proceedings of the IEEE International Conference on Acoustics, Speech and Signal Processing, Vol. 3, April 9-11, 1980, San Francisco, IEEE New York (US) J.L. CALDWELL: "Programmable Synthesis using a New "Speech Microprocessor" pages 868-871 * Abstract *
- ICASSP 79, Proceedings of the IEEE International Conference on Acoustics, Speech and Signal Processing, April 2-4, 1979 Washington, IEEE New York (US) L. NEBBIA et al.: "Eight-Channel Digital Speech Synthesizer Based on LPC Techniques" pages 884-886 * Abstract *
- Computer Design, Vol. 17, No. 9, September 1978 Concord (US) "Monilithic PMOS Speech Synthesizer Models Vocal Tract on Single Chip" pages 200,202 * the whole article *
- Computer Design, Vol. 18, No. 7, July 1979 Concord (US) L. SCHMIDT: "Implementing a Digital Filter Design in Custom LSI-Reducing Multiplier Area" pages 180,182,183 * figures 2,3 *
- Electronics International, Vol. 53, No. 24, November 1980 New York (US) P. HAMILTON: "Speech Processor on Single Chip Talks at Low Bit Rate with Novel Coding Technique" pages 41,42 * the whole article *
- Electronics International, Vol. 54, No. 5, March 10, 1981, New York (US) P. AHRENS et al.: "Speech Chip Timeshares a 2-Pole Section to Create a 12-Pole Filter" pages 177-180 * figures 1,2 *
- Electronic Engineering, Vol. 53, No. 647, January 1981 London (GB) "Speech Synthesis: Devices and Applications" pages 41, 45-47, 49,51,52,54,57 * figure 7 *

Cited by
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DOCDB simple family (application)
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