

Title (en)  
INTEGRATED CURRENT-SOURCE GENERATOR IN CMOS TECHNOLOGY

Publication  
**EP 0052553 B1 19850327 (FR)**

Application  
**EP 81401753 A 19811030**

Priority  
FR 8024232 A 19801114

Abstract (en)  
[origin: US4442398A] An integrated circuit constituting a current generator formed by CMOS technology comprises a first pair of similar transistors, one of which recopies the current of the other, subject to a proportionality factor; a second pair of similar transistors, one of which recopies the source voltage of the other; a third pair of similar transistors having different threshold voltages in contrast to the other pairs. A resistor is placed in series with one of the transistors of the third pair in order to compensate for the difference between the threshold voltages, an additional transistor being provided for recopying the current in one of the transistors aforementioned. The current thus produced is stable in time as well as independent of temperature and of the circuit supply voltage.

IPC 1-7  
**G05F 3/20**

IPC 8 full level  
**G05F 3/24** (2006.01); **G05F 3/26** (2006.01); **H01L 21/8238** (2006.01); **H01L 27/08** (2006.01); **H01L 27/092** (2006.01); **H01L 29/78** (2006.01); **H03F 3/34** (2006.01); **H03F 3/345** (2006.01); **H03F 3/347** (2006.01)

CPC (source: EP US)  
**G05F 3/262** (2013.01 - EP US)

Cited by  
US4618815A; EP0788047A1; US4450367A; EP0064513A4; EP0895354A3; FR2744262A1; EP0627820A3; EP0687967A1; FR2721119A1; US5644216A; WO9750026A1

Designated contracting state (EPC)  
DE FR GB IT NL

DOCDB simple family (publication)  
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