

Title (en)
ELECTRONIC IDENTIFICATION SYSTEM

Publication
EP 0065182 B1 19880629 (FR)

Application
EP 82103815 A 19820504

Priority
FR 8109452 A 19810512

Abstract (en)
[origin: ES8304344A1] The identification system consists of an electronic key comprising a passive memory area (10) and a shift register (9) and a lock capable of being coupled with the key. The lock is capable of supplying a pulse causing the code contained in the memory (10) to be loaded into the register (9). The register (9) is looped on itself via connection (113). Before reading the contents of the register (9) a set number of clock pulses, counted by the control circuit (149) and transmitted by the electronic lock on the H terminal, produces a series of permutations of the contents of the shift register (9). After this permutation phase, the AND gate (157) allows the data contained in the shift register (9) to flow out through the output terminal S due to the action of additional read pulses the number of which is equal to the number of bits in the register (9).

IPC 1-7
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IPC 8 full level
E05B 49/00 (2006.01); **G06K 17/00** (2006.01); **G07C 9/00** (2006.01); **G07F 7/08** (2006.01); **G07F 7/12** (2006.01)

CPC (source: EP US)
G07C 9/00174 (2013.01 - EP US)

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FR 2136269 A5 19721222 - EASTERN CO

Cited by
US5003801A; EP0268140A3; WO9402702A1

Designated contracting state (EPC)
AT BE CH DE GB IT LI LU NL SE

DOCDB simple family (publication)
EP 0065182 A2 19821124; **EP 0065182 A3 19830406**; **EP 0065182 B1 19880629**; AT E35468 T1 19880715; CA 1187991 A 19850528; DE 3278725 D1 19880804; ES 512058 A0 19830216; ES 8304344 A1 19830216; FR 2506047 A1 19821119; FR 2506047 B1 19860207; JP H0418356 B2 19920327; JP S5820881 A 19830207; US 4486751 A 19841204

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