

Title (en)
CIRCUITRY FOR ADDRESSING MATRIX ELEMENTS

Publication
EP 0073429 A3 19840926 (DE)

Application
EP 82107614 A 19820820

Priority
DE 3133785 A 19810826

Abstract (en)
[origin: EP0073429A2] In circuitry for addressing matrix elements having n row electrodes and m column electrodes, two feed lines are in each case provided for each row and/or column electrode. Between each feed line and the associated electrode, a capacitor is located. In each case $2\sqrt{n}$ or $2\sqrt{m}$ of these feed lines are connected to a common input. The number of inputs is preferably $2(\sqrt{n} + \sqrt{m})$. <IMAGE>

IPC 1-7
G09G 3/20

IPC 8 full level
H03K 17/00 (2006.01); **G09G 3/20** (2006.01); **G09G 3/297** (2013.01)

CPC (source: EP)
G09G 3/20 (2013.01); **G09G 3/297** (2013.01); **G09G 2310/0218** (2013.01)

Citation (search report)
• [Y] GB 2042237 A 19800917 - XEROX CORP
• [Y] US 3894506 A 19750715 - MAYER WILLIAM N, et al

Designated contracting state (EPC)
AT CH DE FR GB IT LI NL SE

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EP 0073429 A2 19830309; EP 0073429 A3 19840926; AU 8757682 A 19830303; BR 8204959 A 19830802; DE 3133785 A1 19830310; JP S5846388 A 19830317; ZA 826235 B 19830727

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EP 82107614 A 19820820; AU 8757682 A 19820825; BR 8204959 A 19820825; DE 3133785 A 19810826; JP 14749382 A 19820825; ZA 826235 A 19820826