

Title (en)

CIRCUIT GENERATING A D.C. OUTPUT VOLTAGE FREE OF VOLTAGE VARIATIONS OF THE D.C. SUPPLY

Publication

**EP 0075763 A3 19840718 (DE)**

Application

**EP 82108370 A 19820910**

Priority

DE 3137451 A 19810921

Abstract (en)

[origin: US4423370A] Circuit for generating a d-c output voltage being independent of fluctuations of a d-c supply voltage, including a reference voltage circuit connected to a d-c supply voltage source, the reference voltage circuit including a series circuit of a constant-current source and a potential shift branch, an inverting amplifier being connected to and addressed by the reference voltage circuit, the inverting amplifier having an output circuit including a combination of a plurality of first resistors and at least one first transistor determining the gain of the inverting amplifier, an output driver supplying the d-c output voltage, the output driver being connected to and addressed by the inverting amplifier and the output driver having an output circuit being connected to the potential shift branch of the reference voltage circuit for driving the potential shift branch, the output driver including an emitter follower stage having an output circuit with a second transistor and a second resistor, a voltage stabilizing circuit having a tap carrying a prestabilized voltage and the voltage stabilizing circuit being connected to the d-c supply voltage source, a third resistor connected between the tap and the at least one first transistor in the output circuit of the inverting amplifier, and a fourth resistor connected between the tap and the second transistor in the emitter follower output circuit of the output driver, the first, second, third and fourth resistors having the same resistance value.

IPC 1-7

**G05F 3/20**

IPC 8 full level

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CPC (source: EP US)

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Citation (search report)

- [AD] DE 2849153 A1 19800514 - SIEMENS AG
- [A] DE 2849231 A1 19800514 - SIEMENS AG
- [A] GB 2068608 A 19810812 - PHILIPS NV
- [A] DE 2533199 A1 19770127 - SIEMENS AG
- [AD] IEEE JOURNAL OF SOLID STATE CIRCUITS, Band SC-7, 1972, Seiten 267-269, New York, (USA)

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