

Title (en)

INTEGRATED CIRCUIT MICROPROCESSOR-CONTROLLED TELEPHONE TWO WIRE/FOUR WIRE TRANSMISSION CIRCUIT, AND LINE CURRENT SAFEGUARD POWERING

Publication

EP 0080396 B1 19860430 (FR)

Application

EP 82402054 A 19821109

Priority

FR 8121803 A 19811120

Abstract (en)

[origin: US4562309A] The telephone circuit of the invention comprises a transmission circuit short-circuited during decimal numbering by an active circuit when the handset is lifted or during the emission of a decimal numbering digit, the line current supplying, through a voltage stabilizing circuit and a voltage regulation circuit, the microprocessor controlling the telephone circuit.

IPC 1-7

H04M 19/08

IPC 8 full level

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CPC (source: EP US)

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Cited by

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