Title (en)
SEMICONDUCTOR DEVICE HAVING AN INTERCONNECTION PATTERN

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Application
EP 82201629 A 19821220

Priority
NL 8105920 A 19811231

Abstract (en)
[origin: EP0083816A1] With the aid of anisotropic etching techniques, such as plasma etching and reactive ion etching, inter-connection patterns can be obtained having accurately defined rims. According to the invention, these rims are provided inter alia at the area of contacts to be formed. The contacting zone thus formed, which has an accurately defined form, can be manufactured with rims of, for example, a refractory metal or polycrystalline silicon. In the latter case, a semiconductor zone (emitter zone, base contact zone, source, drain etc.) can be formed through the polycrystalline silicon in the subjacent semiconductor body. In this manner, very small structures can be obtained, whilst moreover different kinds of transistors can be manufactured in the same semiconductor body.

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IPC 8 full level
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Citation (examination)
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