

Title (en)  
Apparatus for error correction.

Title (de)  
Fehlerkorrekturanordnung.

Title (fr)  
Dispositif de correction d'erreurs.

Publication  
**EP 0086566 A2 19830824 (EN)**

Application  
**EP 83300264 A 19830119**

Priority  
JP 654382 A 19820119

Abstract (en)

In an apparatus for error correction in which data sequences containing blocks each formed of plural data words previously arranged on a time-base and check words are written in a data memory (37) on the basis of a write address generator (38), the data sequences are then read out from the data memory (37) on the basis of a read address generator (39) so as to generate rearranged data sequences and, during writing and reading of the data sequences, error correction is carried out, there is included an error correction arithmetic circuit (50) for performing an error correction calculation, a pointer addition circuit (51, 52) for adding a pointer to the data words in association with an error state of the blocks, and a program memory (49) for memorizing a microprogramm with fields to control the error correction arithmetic circuit (50) and pointer addition circuit (51, 52).

IPC 1-7  
**G11B 5/09; H04L 1/10**

IPC 8 full level  
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CPC (source: EP US)  
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Cited by  
EP0130091A1; EP0232133A3; EP0180470A3; EP0156413A1; EP0569716A3; US5528608A; US5898710A; US5968200A; EP0748058A3;  
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