

Title (en)

EEPROM WITH BULK ZERO PROGRAM CAPABILITY.

Title (de)

EEPROM MIT DER FÄHIGKEIT ZUR GANZNULLPROGRAMMIERUNG.

Title (fr)

EEPROM POSSEDANT UNE CAPACITE DE PROGRAMME D'ANNULATION DE MASSE.

Publication

**EP 0089364 A4 19850425 (EN)**

Application

**EP 82903017 A 19820917**

Priority

US 30611981 A 19810928

Abstract (en)

[origin: WO8301146A1] An EEPROM (10) which can erase an array (36) of memory cells to all "1"s is capable of causing all of the cells of the array (36) to assume all "0"s, also. The array (36) of memory cells is arranged with one of said memory elements at the intersection of each of a plurality of rows and columns. The memory elements are characterized as assuming relatively high threshold voltage states in response to an erase voltage and relatively low threshold voltage states in response to a program voltage. All columns and rows are selected in order to apply the program voltage to all of the memory elements.

IPC 1-7

**G11C 11/40**

IPC 8 full level

**G11C 14/00** (2006.01); **G11C 11/41** (2006.01); **G11C 16/08** (2006.01); **G11C 16/16** (2006.01); **G11C 16/32** (2006.01); **G11C 17/00** (2006.01)

CPC (source: EP US)

**G11C 16/08** (2013.01 - EP US); **G11C 16/16** (2013.01 - EP US); **G11C 16/32** (2013.01 - EP US); **G11C 2216/16** (2013.01 - EP US); **G11C 2216/18** (2013.01 - EP US)

Citation (search report)

- [A] GB 2053611 A 19810204 - MOSTEK CORP
- [A] EP 0023798 A2 19810211 - FUJITSU LTD [JP]
- [A] PATENTS ABSTRACTS OF JAPAN, vol. 5, no. 77 (P-62)[749], May 21, 1981, & JP - A - 56 25 295 (NIPPON DENKI K.K.) 11-03-1981.

Designated contracting state (EPC)

AT DE FR GB NL

DOCDB simple family (publication)

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DOCDB simple family (application)

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