

Title (en)  
JOSEPHSON LOGIC CIRCUIT

Publication  
**EP 0095979 B1 19861029 (EN)**

Application  
**EP 83401069 A 19830527**

Priority  
• JP 9280282 A 19820531  
• JP 22970782 A 19821228

Abstract (en)  
[origin: EP0095979A2] A Josephson logic circuit includes a Josephson element (J1) inserted between an input terminal (IN) and a reference electric potential such as the ground, a resistor (R01) inserted between the input terminal and an input of the next stage (B) of the Josephson circuit, and a current source which supplies an offset current (Ioff) to the Josephson element. By adjusting the offset current, the Josephson logic circuit can be operated as an AND circuit, an OR circuit, or a majority logic circuit having a high input sensitivity.

IPC 1-7  
**H03K 19/195**

IPC 8 full level  
**H03K 19/195** (2006.01)

CPC (source: EP US)  
**H03K 19/1956** (2013.01 - EP US)

Citation (examination)  
EP 0081922 A1 19830622 - SPERRY CORP [US]

Cited by  
EP0527557A1; US5233243A; US5170080A

Designated contracting state (EPC)  
DE FR GB

DOCDB simple family (publication)  
**EP 0095979 A2 19831207; EP 0095979 A3 19840425; EP 0095979 B1 19861029**; DE 3367310 D1 19861204; US 5455519 A 19951003

DOCDB simple family (application)  
**EP 83401069 A 19830527**; DE 3367310 T 19830527; US 2807587 A 19870320