Title (en)

AUTOMATIC CIRCUIT IDENTIFIER.

Title (de)

AUTOMATISCHER SCHALTUNGSIDENTIFIZIERER.

Title (fr)

IDENTIFICATEUR AUTOMATIQUE DE CIRCUIT.

Publication

EP 0104169 A4 19860107 (EN)

Application

EP 82901340 A 19820330

Priority

US 8200399 W 19820330

Abstract (en)

[origin: WO8303488A1] A hand-held apparatus for automatically identifying a hybrid or monolithic integrated circuit or any standard circuit module having a known pin configuration (10) and an unknown identity. A plurality of test instructions for devices of known identity are stored in a test procedure memory (22) and sequentially provided (125) to the unit under test until the unit either passes a test thus revealing its identity (137) or fails all tests in the memory (145). The socket (10) is connected to a pair of peripheral chips (25a, 25b) having pin definable input/output ports (46, 65, 66) which are used selectively to treat any pin as input, output, or supply.

IPC 1-7

G06F 11/22

IPC 8 full level

G06F 11/22 (2006.01); G01R 31/3193 (2006.01); G06F 11/00 (2006.01)

CPC (source: EP)

G01R 31/3193 (2013.01); G06F 11/006 (2013.01)

Citation (search report)

- [A] DE 2631483 A1 19780119 - SIEMENS AG
- [A] US 4055802 A 19771025 - PANOUSIS PETER THEODORE, et al
- [A] IBM TECHNICAL DISCLOSURE BULLETIN, vol. 22, no. 5, October 1979, pages 1879-1880, New York, US; C.J. KUGLER et al.: "Array chip identification"
- [A] ELECTRONIC ENGINEERING, vol. 50, no. 606, May 1978, page 32, London, GB; C.R. SELVAKUMAR: "A versatile quick check linear IC tester"

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

WO 8303488 A1 19831013; EP 0104169 A1 19840404; EP 0104169 A4 19860107; JP S59500437 A 19840315

DOCDB simple family (application)

US 8200399 W 19820330; EP 82901340 A 19820330; JP 50138982 A 19820330