

Title (en)
DATA WRITE ARRANGEMENT FOR COLOR GRAPHIC DISPLAY UNIT

Publication
EP 0105724 A3 19860226 (EN)

Application
EP 83305876 A 19830929

Priority
JP 17246182 A 19820929

Abstract (en)
[origin: EP0105724A2] A plurality of color graphic memories (41,51,61) are assigned the same address space, and are connected to a common data bus (X1). It is determined by a separately output memory select data which one of the color graphic memories (41,51,61) is to be written into. In the case of writing the same data into two or more of the color graphic memories (41,51,61) at the same address, the color graphic memories to be written into are selected by the memory select data first and then the same data is written into the selected colour graphic memories in the same write cycle.

IPC 1-7
G09G 1/28

IPC 8 full level
G09G 5/39 (2006.01); **G09G 5/02** (2006.01); **G09G 5/395** (2006.01)

CPC (source: EP)
G09G 5/022 (2013.01)

Citation (search report)
[X] US 4016544 A 19770405 - MORITA TAKAYA, et al

Cited by
EP0313789A1; US4811007A; EP0148564A3; US4823119A; US4789963A

Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
EP 0105724 A2 19840418; EP 0105724 A3 19860226; EP 0105724 B1 19900214; DE 3381222 D1 19900322; JP S5960488 A 19840406

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