

Title (en)
Method of controlling a matrix display.

Title (de)
Verfahren zum Steuern einer Matrixanzeigeeinrichtung.

Title (fr)
Procédé de commande d'un imageur matriciel.

Publication
EP 0105767 A1 19840418 (FR)

Application
EP 83401688 A 19830819

Priority
FR 8214644 A 19820826

Abstract (en)
[origin: US4541690A] Control process for a matrix display comprising a material, whereof an optical property is to be modified, said material being placed between a first group of p parallel electrode rows and a second group of q parallel electrode columns, the rows and columns crossing one another, an area ij of the material being defined by the region of the material covered by row i, in which i is an integer such that $1 \leq i \leq p$, and by the column j, in which j is an integer such that $1 \leq j \leq q$, the rows and columns being used for carrying signals bringing about an excitation of the material, wherein the electrode rows have n horizontal gaps defining n+1 identical sets of electrode columns, a signal I is applied to electrode row i, while a zero signal is applied to the other electrode rows, the signal I being sequentially applied to the p electrode rows in accordance with increasing values of i, and wherein a signal J is applied to the electrode columns, said signal J being simultaneously applied to the electrode columns of the first set, during the application time of signal I to the p/n first electrode rows, the electrode columns of the other sets receiving a zero signal, then the electrode columns of the second set during the application time of signal I to the p/n following electrode rows, the electrode columns of the first set and other sets receiving a zero signal and so on up to the excitation of the electrode columns of the nth set.

Abstract (fr)
L'invention a pour objet un procédé de commande d'un imageur matriciel, comportant p lignes d'électrodes et q colonnes d'électrodes, consistant à appliquer sur la ligne un signal I, i étant un entier tel que $1 \leq i \leq p$, et sur les autres lignes un signal nul, le signal I étant appliqué séquentiellement aux p lignes suivant les valeurs croissantes de i, et à appliquer sur les colonnes un signal J, ce signal J étant appliqué simultanément sur les colonnes du premier jeu (d), pendant le temps d'application du signal I sur les p/n premières lignes (1, 2, 3, 4, 5), les colonnes des autres jeux (e) recevant un signal nul, puis sur les colonnes du deuxième jeu (e), pendant le temps d'application du signal I sur les p/n lignes suivantes (6, 7, 8, 9, 10), les colonnes des autres jeux (d) recevant un signal nul, et ainsi de suite jusqu'à excitation des colonnes du n-ième jeu.

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G09G 3/36

IPC 8 full level
G02F 1/133 (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP US)
G09G 3/3644 (2013.01 - EP US)

Citation (search report)
• US 4281324 A 19810728 - NONOMURA KEISAKU, et al
• FR 2443699 A1 19800704 - SEIKO INSTR & ELECTRONICS [JP]

Cited by
EP0315365A3; FR2542119A1

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DE GB IT NL

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FR 8214644 A 19820826; CA 435361 A 19830825; DE 3369020 T 19830819; EP 83401688 A 19830819; JP 15569583 A 19830825; US 52423283 A 19830818