

Title (en)
CIRCUIT FOR FAIL SAFE CONTROL OF A SYSTEM

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Application
EP 83201384 A 19830927

Priority
NL 8203734 A 19820927

Abstract (en)
[origin: EP0109696A1] The invention relates to a circuit for fail safe control of a system comprising a command switch, closing of which activates a control relay so that through the switched over contacts of said control relay a holding path for said control relay is closed. The circuit furthermore comprises an auxiliary relay and a capacitor, which capacitor after closing of the command switch only through at least a normally closed contact of the control relay can be charged to a predetermined voltage before the control relay is energized. Said control relay is energized through an energizing path containing at least one contact of the auxiliary relay offering the possibility to check a switching device, and is maintained in the energized state through a contact of the control relay. The capacitor is after energizing of the control relay connected to the auxiliary relay through at least one of the now switched over contacts of said control relay and forms during a predetermined period the only voltage source for said auxiliary relay, energizing said auxiliary relay.

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