

Title (en)

CIRCUIT ARRANGEMENT FOR OPERATING HIGH-PRESSURE DISCHARGE LAMPS

Publication

**EP 0111956 B1 19861029 (DE)**

Application

**EP 83201696 A 19831201**

Priority

DE 3245924 A 19821211

Abstract (en)

[origin: CA1229128A] Circuit arrangement for operating a high-pressure gas discharge lamp. In a circuit arrangement for operating a highpressure gas discharge lamp (3) with a pulsatory direct current, which is produced from an alternating voltage mains (A, B) via a full-wave rectifier (1), the output of the full-wave rectifier being shunted by a series arrangement of a diode (4) and a capacitor (5), according to the invention, the capacitor (5) has a value of 10 nF to 1 .mu.F and a resistor (6), which is high-ohmic with respect to a current limiter (2) in series with the lamp (3), is connected in a current circuit between the end of this capacitor facing the diode and the lamp (3). As a result, inter alia a low re-ignition voltage is attained at the heating-up stage of the lamp. Fig. 1.

IPC 1-7

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IPC 8 full level

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