

Title (en)  
VOLTAGE TRANSIENT PROTECTION CIRCUIT

Publication  
**EP 0115002 B1 19890208 (EN)**

Application  
**EP 83112700 A 19831216**

Priority  
US 45340282 A 19821227

Abstract (en)  
[origin: EP0115002A2] A three terminal integrated low-resistant series switch circuit (10) which buffers load electronics from extreme input voltage conditions. The circuit comprises a series pass transistor (20) the conductivity of which is controlled by control circuitry (22, 24, 28, 32) in cooperation with feedback control (36). In response to the input exceeding a predetermined operating potential the series pass transistor is driven toward saturation and is maintained in this state by feedback whereby only that load current required by the load electronics is supplied for increased efficiency. The control circuitry is responsive to the signal at the input thereof exceeding a predetermined level for rendering the series pass transistor nonconducting to protect the load electronics from extreme voltage excursions. A feature of the circuit is that the epitaxial layer within which the control circuit is formed is operated at a lower voltage than that of the substrate.

IPC 1-7  
**H02H 7/20**; H02H 3/02; G05F 1/569

IPC 8 full level  
**H02H 3/10** (2006.01); **H02H 7/20** (2006.01); **H03K 17/08** (2006.01); **H03K 17/60** (2006.01)

CPC (source: EP US)  
**H02H 7/20** (2013.01 - EP US)

Citation (examination)  
US 4319181 A 19820309 - WRATHALL ROBERT S

Cited by  
US4772979A; EP0422487A3

Designated contracting state (EPC)  
DE FR GB IT NL

DOCDB simple family (publication)  
**EP 0115002 A2 19840808**; **EP 0115002 A3 19851121**; **EP 0115002 B1 19890208**; DE 3379190 D1 19890316; HK 23491 A 19910404; JP H0580846 B2 19931110; JP S59136019 A 19840804; US 4495536 A 19850122

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**EP 83112700 A 19831216**; DE 3379190 T 19831216; HK 23491 A 19910326; JP 24366283 A 19831223; US 45340282 A 19821227