Title (en)
DATA CAPTURE WINDOW EXTENSION CIRCUIT

Publication
EP 0119766 B1 19871111 (EN)

Application
EP 84301152 A 19840222

Priority
US 46812983 A 19830222

Abstract (en)
[origin: US4550391A] A digital data capture window extension circuit utilizing a matched pair of signal detection subcircuits that are activated during alternate cycles of a master read clock. The interleaved operation of the paired subcircuits permits each subcircuit to detect and process an incoming data signal, resulting in an effective overall processing rate twice that of prior art circuitry. The invention therefore permits the reading of data at a high rate using a system clock of the same frequency as the incoming data.

IPC 1-7
G11B 5/09

IPC 8 full level
H04L 25/40 (2006.01); **G11B 5/09** (2006.01); **G11B 20/14** (2006.01); **H04L 7/00** (2006.01)

CPC (source: EP US)
G11B 20/1403 (2013.01 - EP US); **G11B 20/1419** (2013.01 - EP US); **G11B 20/1423** (2013.01 - EP US)

Cited by
US6341326B1; WO0039692A1

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DOCDB simple family (publication)
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EP 84301152 A 19840222; AT 84301152 T 19840222; DE 3467444 T 19840222; JP 3226384 A 19840222; US 46812983 A 19830222