Title (en)

Plasma display management systems.

Title (de)

System zur Steuerung von Plasmabildschirmen.

Title (fr)

Système de gestion de panneaux d'affichage à plasma.

Publication

EP 0121070 A2 19841010 (EN)

Application

EP 84101729 A 19840220

Priority

- US 47277683 A 19830307
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Abstract (en)

A display management system for one or more large plasma gas panel displays (11) is organised to split the display management operations between a host processor (20), a system microprocessor (21) and a picoprocessor (27). The picoprocessor (27) is the heart of a common internal bus (31) plasma display adapter (22). The program for the display is downloaded from the host processor (20) to the system microprocessor (21). The microprocessor generates op codes and initialises display parameters. The plasma display adapter provides control for the plasma panel interface, serialisation of character generator data, translates display position addresses for absolute cartesian coordinates to panel address, and computes the boundaries of dislay panel write and erase operations. Interaction between the system microprocessor and the plasma display adapter is minimised by a code list contained in the microprocessor memory and fetched by direct memory access. This code list contains high level commands which the picoprocessor in the adapter decodes and translates into simple commands for the surrounding interface logic.

IPC 1-7

G06F 3/147; G09G 3/28

IPC 8 full level

G06F 3/147 (2006.01); G09G 3/28 (2013.01)

CPC (source: EP)

G09G 5/14 (2013.01); **G09G 5/222** (2013.01); **G09G 5/363** (2013.01); G09G 3/2922 (2013.01); G09G 3/293 (2013.01); G09G 2310/0286 (2013.01)

Cited by

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