

Title (en)

METHOD AND DEVICE FOR ELIMINATING INTERFERENCES DUE TO LOAD VARIATION IN SWITCHED-MODE POWER SUPPLIES

Publication

EP 0128988 B1 19870902 (FR)

Application

EP 83401229 A 19830615

Priority

EP 83401229 A 19830615

Abstract (en)

[origin: EP0128988A1] 1. A process for controlling a chopped power supply connected to a supply network and comprising a primary inductance (Lp) coupled through a common magnetic circuit to a secondary inductance (Ls) and supplying a load (X) to which a capacitor (C) is connected in parallel, said process consisting in completely demagnetizing the magnetic circuit for each period of the chopping, characterized in that, to this end, the value of the secondary inductance (Ls) is automatically adapted as a function of the value of the voltage (Us) at the terminals of the load (X).

IPC 1-7

H02M 3/335; **H02M 3/155**

IPC 8 full level

G05F 1/20 (2006.01); **H05B 41/40** (2006.01)

CPC (source: EP)

G05F 1/20 (2013.01); **H05B 41/40** (2013.01)

Cited by

US11801460B2

Designated contracting state (EPC)

AT BE CH DE GB IT LI LU NL SE

DOCDB simple family (publication)

EP 0128988 A1 19841227; **EP 0128988 B1 19870902**; AT E29353 T1 19870915; DE 3373388 D1 19871008

DOCDB simple family (application)

EP 83401229 A 19830615; AT 83401229 T 19830615; DE 3373388 T 19830615