

Title (en)
Semiconductor device having a heterojunction.

Title (de)
Halbleiteranordnung mit einem Heteroübergang.

Title (fr)
Dispositif semi-conducteur ayant une hétéro-jonction.

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Application
EP 84105301 A 19840510

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JP 8422383 A 19830516

Abstract (en)
According to the present invention, a semiconductor device having first semiconductor layer (22) and a second semiconductor layer (23) smaller in a forbidden band gap than the first semiconductor layer (22), the first and second semiconductor layers being arranged so as to form a heterojunction, a first electrode (28) for controlling carriers (32) generated at least in the vicinity of the heterojunction interface, and at least two second electrodes (29, 30); is so constructed that the second semiconductor layer (23) exists between the first semiconductor layer (22) and the first electrode (28) for controlling the carriers (32), that both the first and second semiconductor layers (22, 23) contain an impurity at a concentration of only at most 10^{15} cm⁻³ and that an impurity (24) identical in a conductivity type to the carriers (32) is contained at a concentration of at least 10^{16} cm⁻³ in regions (25, 26) located between the second electrodes (29, 30) and a region where the carriers (32) are generated, so as to electrically connect the second electrodes (29, 30) and the carriers (32) generated in the vicinity of the heterojunction interface under the first electrode (28) for controlling the carriers. Elements of both the enhancement and depletion modes can be readily fabricated distinctively.

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H01L 29/80; H01L 29/10

IPC 8 full level
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