

Title (en)  
CIRCUIT ARRANGEMENT FOR OPERATING HIGH-PRESSURE GAS DISCHARGE LAMPS

Publication  
**EP 0134050 B1 19871119 (DE)**

Application  
**EP 84201073 A 19840718**

Priority  
DE 3327030 A 19830727

Abstract (en)  
[origin: US4594531A] A circuit arrangement for operating high-pressure gas discharge lamps (6) with a current of higher frequency comprising a switching mains section including a switching transistor (4) and a control device (9), in which an upper and a lower reference current level (O,U) are produced. The switching transistor is switched to the non-conducting state when the lamp current exceeds the upper level (O) and is switched to the conducting state when this current falls below the lower level (U). The interval between the reference current levels (O,U) is more than 10% of the average lamp current and a further intermediate reference current level (M) is provided at which the switching transistor is switched each time at a predetermined number of passages of the lamp current through the intermediate reference current level.

IPC 1-7  
**H05B 41/14**

IPC 8 full level  
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