

Title (en)
DATA RECEIVER

Publication
EP 0140440 A3 19860226 (EN)

Application
EP 84201489 A 19841016

Priority
GB 8328162 A 19831021

Abstract (en)
[origin: EP0140440A2] A data receiver in which the phase of the carrier signal is controlled so that the threshold levels used for coherent demodulation occur at the quarter points, that is 0°, 90°, 180° and 270°. Generally an input signal is mixed with a local oscillator signal in a pair of mixers (10, 12) and the outputs therefrom are low pass filtered and subsequently demodulated. Any phase errors between the local oscillator signal and the input carrier signal have been corrected by a correction loop connected to the local oscillator. Such a means of correction is limited in that the rate at which the phase correction can be effected is determined by the time delay due to the low pass filters and this can be a disadvantage in obtaining fast acquisition. By correcting the carrier phase error after (or downstream of) the low pass filters (14, 16) then the phase can be corrected rapidly without the risk of instability. In Figure 1 the phase can be carried by means of phase shifting networks (18, 20) having parallel outputs, the selection of a particular output is determined by a control signal from the data demodulator (36).

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H01L 27/14

IPC 8 full level
H04L 27/38 (2006.01); **H04L 27/152** (2006.01); **H04L 27/16** (2006.01); **H04L 27/227** (2006.01)

CPC (source: EP US)
H04L 27/1525 (2013.01 - EP US); **H04L 27/16** (2013.01 - EP US); **H04L 27/2273** (2013.01 - EP US)

Citation (search report)
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Designated contracting state (EPC)
DE FR GB IT SE

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EP 0140440 A2 19850508; EP 0140440 A3 19860226; EP 0140440 B1 19890809; DE 3479367 D1 19890914; GB 2148669 A 19850530; GB 8328162 D0 19831221; JP H0657024 B2 19940727; JP S60103855 A 19850608; US 4682117 A 19870721

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