Title (en)

JITTER-IMMUNE TIME EXPANSION FOR TIME-COMPRESSED LINE-SEQUENTIAL VIDEO SIGNALS

Publication

EP 0140706 A3 19860305 (EN)

Application

EP 84307476 A 19841030

Priority

JP 20563183 A 19831031

Abstract (en)

[origin: EP0140706A2] In an apparatus for time-compression video recording and time-expansion reproduction, first and second memory address counters are initialized in response to there being a simultaneous presence of vertical and horizontal sync pulses. Until the next vertical blanking period the counters are continuously incremented at different rates during mutually exclusive times for successively writing and reading the luminance component of a composite color television signal. A third memory address counter operable in recyclically variable count modes is provided for writing and reading the color-difference components at sequentially varying rates during a count cycle and generating an output pulse at the end of the count cycle. A phase comparator detects the phase difference between the horizontal sync pulse and the output pulse of the third address counter. A variable frequency oscillator is responsive to the phase difference for incrementing the third address counter at a rate corresponding to the phase difference.

IPC 1-7

H04N 9/81

IPC 8 full level

G11B 20/06 (2006.01); **H04N 9/81** (2006.01); **H04N 9/896** (2006.01)

CPC (source: EP KR)

G11B 20/06 (2013.01 - KR); **H04N 9/81** (2013.01 - EP KR); **H04N 9/896** (2013.01 - EP)

Citation (search report)

- [X] US 4335393 A 19820615 - PEARSON GILBERT J
- [A] US 4210927 A 19800701 - FURUHATA TAKASHI [JP], et al
- [YP] DE 3323444 A1 19831229 - SONY CORP [JP]

Designated contracting state (EPC)

AT DE FR GB NL

DOCDB simple family (publication)

EP 0140706 A2 19850508; **EP 0140706 A3 19860305**; **EP 0140706 B1 19901227**; AT E59518 T1 19910115; DE 3483872 D1 19910207; JP S6096985 A 19850530; KR 850003020 A 19850528; KR 900001450 B1 19900310

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