

Title (en)
SHORT-PROTECTED BUFFER CIRCUIT.

Title (de)
VOR KURZSCHLUSS GESCHÜTZTE PUFFERSCHALTUNG.

Title (fr)
CIRCUIT DE TAMPON PROTEGE CONTRE LES COURTS-CIRCUITS.

Publication
EP 0141819 A4 19860605 (EN)

Application
EP 84900356 A 19831212

Priority
US 46369783 A 19830204

Abstract (en)
[origin: WO8403181A1] A short-protected buffer circuit comprising a source current output transistor (T1, Fig. 3) and a sink current output transistor (T2) is capable of providing high output current at low output voltage. A current-limiting bypass circuit, comprising two series-connected field effect transistors (T3 and T4) and a voltage reference (T5 and T6), limits the gate voltage on the source current output transistor (T1) and thus limits the output source current. The circuit is compact and can be implemented in integrated circuit form. The output voltage at which protection starts is independent of temperature and of process variations.

IPC 1-7
H02H 3/08; H02H 3/20; H02H 3/28

IPC 8 full level
H02H 9/02 (2006.01); **H02H 7/20** (2006.01); **H03K 19/003** (2006.01); **H03M 7/00** (2006.01)

CPC (source: EP KR)
H03K 19/00315 (2013.01 - EP); **H03M 7/00** (2013.01 - KR)

Citation (search report)

- No relevant documents have been disclosed
- No relevant documents have been disclosed

Designated contracting state (EPC)
BE DE FR GB NL SE

DOCDB simple family (publication)
WO 8403181 A1 19840816; CA 1191560 A 19850806; EP 0141819 A1 19850522; EP 0141819 A4 19860605; IT 1178355 B 19870909; IT 8447534 A0 19840112; JP S60500437 A 19850328; KR 840008098 A 19841212; KR 900001812 B1 19900324

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US 8301966 W 19831212; CA 443506 A 19831216; EP 84900356 A 19831212; IT 4753484 A 19840112; JP 50045784 A 19831212; KR 840000525 A 19840204