

Title (en)
DEVICE FOR THE INTERRUPTION OF CURRENTS

Publication
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Application
EP 84112088 A 19841009

Priority
JP 18965883 A 19831011

Abstract (en)
[origin: EP0142022A1] 1. A current-interruption arrangement, preferably short-circuit currents, in three-phase networks (R, S, T), wherein the current of one phase (T) does not pass through zero initially, separate switches being controlled by a current detection device (3-8) having zero current transit detection devices (3) for the phases, characterised in that the current detection device (3-8) in each case detects that (S) of the phases (R, S) passing through zero, wherein the difference of the adjacent time intervals (C-D, D-G) defined by three consecutive zero transitions (C, D, G) is greater than in the case of the other phase (R) and the phase (S), in which the difference is greater, interrupts first.

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IPC 8 full level
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