Title (en)
COIN VALIDATORS

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Application
EP 84307619 A 19841105

Priority
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Abstract (en)
[origin: WO8502047A1] A coin validator comprises a microprocessor (4) which is periodically activated to check the output of an arrival sensing circuit (6) to determine whether a coin has arrived. The circuit (6) includes a printed circuit inductance (10) and produces an output signal the frequency of which alters when a coin arrives and is used to indicate coin diameter. The circuit (6) is designed so that output amplitude variations are minimised. Arrival of a coin causes powering-up of a material/thickness sensing circuit (8). In one embodiment both the frequency and amplitude of the output signal from this circuit are used to determine the denomination of the inserted coin. The amplitude profile may be used to determine when to measure frequency, and which of a plurality of succesive amplitude measurements are to be combined to provide an averaged amplitude value. A counter (24) is used for frequency and amplitude measurement, and for periodically activating the microprocessor (4). Coins are rejected if they travel too quickly through a testing station of the validator. The validator is operable in a test mode to indicate, in response to an inserted coin, how the sensing circuits (6, 8) should be adjusted to provide optimum operation.

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G06F 1/00; G07D 5/08; G07F 3/02

IPC 8 full level
G06F 1/00 (2006.01); **G07D 5/08** (2006.01)

CPC (source: EP)
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Cited by
TR27246A; EP0373948A3; BE1016016A3; EP0384374A1; EP0704825A1; AU654907B2; US5145046A; GB2234619A; GB2234619B; US5337877A; EP0384375A1; US6398001B1; WO9102334A1; WO9404998A1; WO9318489A1

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