

Title (en)  
Asynchronous buffer with stacked registers.

Title (de)  
Asynchroner Puffer mit gestapelten Registern.

Title (fr)  
File d'attente asynchrone à empilement de registres.

Publication  
**EP 0146418 A1 19850626 (FR)**

Application  
**EP 84401952 A 19840928**

Priority  
FR 8315510 A 19830929

Abstract (en)  
[origin: CA1216371A] Asynchronous FIFO device comprising a stack of registers An asynchronous FIFO device suitable for use as a buffer comprises a stack having a plurality of sections. Each section has a data storage register and a control subassembly. Each assembly is associated with one of said data storage registers. A single data input is connected to the first data storage register. The data storage registers have a transparent condition and a latched condition and each subassembly comprises a 2-to-1 MUX having a first input connected to receive a logic signal indicative of the condition of the preceding subassembly, a second input connected to receive a logic signal indicative of the condition of the following subassembly and an output connected to the associated storage register. The MUX is constructed to deliver on its output a signal representative of the condition of the subassembly and its internal connections are determined by the logic level of the output signal of the MUX.

Abstract (fr)  
La file d'attente, utilisable en informatique et traitement numérique du signal, comprend un empilement en cascade de registres identiques (10) de stockage de données et une chaîne logique de commande de sous-ensembles identiques associés chacun à un des registres. Les registres (10) sont du type à verrouillage ayant un état transparent et un état verrouillé, chaque sous-ensemble comportant un commutateur (16) constitué de façon à fournir, sur sa sortie reliée au registre associé, un signal logique représentatif de l'état de sous-ensemble amont ou aval suivant que le registre associé au sous-ensemble comprenant le commutateur est libre ou occupé.

IPC 1-7  
**G06F 13/00**; **G11C 19/00**; **H03K 23/66**

IPC 8 full level  
**G06F 9/46** (2006.01); **G06F 5/08** (2006.01); **G11C 7/00** (2006.01); **H03K 23/66** (2006.01)

CPC (source: EP US)  
**G06F 5/08** (2013.01 - EP US); **H03K 23/665** (2013.01 - EP US)

Citation (search report)

- [AD] GB 2009984 A 19790620 - PHILIPS NV
- [A] US 3953838 A 19760427 - GILBERG ROBERT CHARLES, et al
- [A] US 3378776 A 19680416 - JACOB GOLDBERG, et al

Cited by  
EP0416513A3; US5157633A

Designated contracting state (EPC)  
BE CH DE GB IT LI NL SE

DOCDB simple family (publication)  
**FR 2552916 A1 19850405**; **FR 2552916 B1 19880610**; CA 1216371 A 19870106; DE 3468558 D1 19880211; EP 0146418 A1 19850626; EP 0146418 B1 19880107; JP H0554138 B2 19930811; JP S6093556 A 19850525; US 4819201 A 19890404

DOCDB simple family (application)  
**FR 8315510 A 19830929**; CA 464349 A 19840928; DE 3468558 T 19840928; EP 84401952 A 19840928; JP 20392184 A 19840928; US 7799987 A 19870727