

Title (en)
HIGH RESOLUTION VIDEO GRAPHICS SYSTEM

Publication
EP 0148564 B1 19920506 (EN)

Application
EP 84307409 A 19841026

Priority
US 55617883 A 19831129

Abstract (en)
[origin: EP0148564A2] A high resolution video graphics system for use with a processor and characterized by improved memory expandability particularly in connection with color display. The high resolution video graphics system includes a video memory which is preferably comprised of a plurality of video memory planes and video counter means for providing display timing. There are basically four buses that are used with the system including a control bus, a read bus, a write bus, and an address bus. A multiplexer means is provided for receiving either video counter addresses or direct addresses from the processor. There is associated with the video memory means for converting to serial output data which the preferred embodiment includes a series of shift registers corresponding to the number of separate memory planes employed. In the preferred embodiment the system has three memory planes, each of 16K by 16 capacity. The shift register output is coupled to a high speed static palette RAM with the shift register outputs being the addresses thereto. The data outputs of the palette RAM couple to guns on a color monitor or to the back plane in connection with black and white graphics.

IPC 1-7
G09G 1/16; **G09G 1/28**

IPC 8 full level
G09G 1/16 (2006.01); **G09G 1/28** (2006.01); **G09G 5/06** (2006.01); **G09G 5/39** (2006.01)

CPC (source: EP)
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EP 0148564 A2 19850717; **EP 0148564 A3 19880210**; **EP 0148564 B1 19920506**; AT E75872 T1 19920515; AU 3475184 A 19850606; CA 1237207 A 19880524; DE 3485705 D1 19920611

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