

Title (en)
MULTI-STEP PARALLEL ANALOG-DIGITAL CONVERTER

Publication
EP 0153778 B1 19921209 (EN)

Application
EP 85200176 A 19850212

Priority
US 58041084 A 19840215

Abstract (en)
[origin: EP0153778A2] A multi-step AID converter of the successive approximation type utilizes a single three-position switchable current-output DAC in combination with a voltage divider, a plurality of comparators, a decoder, a successive approximation register and a control logic module to provide a high speed, high resolution A/D converter requiring fewer parts than the prior art A/D converters of this general type.

IPC 1-7
H03M 1/38

IPC 8 full level
H03M 1/14 (2006.01); **H03M 1/00** (2006.01)

CPC (source: EP US)
H03M 1/144 (2013.01 - EP US); **H03M 1/46** (2013.01 - EP US); **H03M 1/68** (2013.01 - EP US); **H03M 1/745** (2013.01 - EP US);
H03M 1/785 (2013.01 - EP US)

Cited by
US5870052A; EP0629049A3

Designated contracting state (EPC)
DE FR GB IT

DOCDB simple family (publication)
EP 0153778 A2 19850904; EP 0153778 A3 19890419; EP 0153778 B1 19921209; CA 1235517 A 19880419; DE 3586877 D1 19930121;
DE 3586877 T2 19930617; JP H0525409 B2 19930412; JP S60217731 A 19851031; US 4649371 A 19870310

DOCDB simple family (application)
EP 85200176 A 19850212; CA 474309 A 19850214; DE 3586877 T 19850212; JP 2531985 A 19850214; US 58041084 A 19840215