

Title (en)

DISPLAY CONTROL UNITE

Publication

EP 0155499 A3 19900912 (EN)

Application

EP 85101561 A 19850213

Priority

JP 2878484 A 19840220

Abstract (en)

[origin: EP0155499A2] A display control unit (3) is disclosed which is able to change the processing form of a central processing unit (1) (CPU) for increased processing speeds. In this display control unit (3), when the execution time of its display operation is reduced by reading out data in a source area from a memory unit (4) and then writing the read-out data sequentially into a destination area, a predetermined period of time before the beginning of a vertical or horizontal retrace, the CPU(1) is interrupted so that the CPU (1) can omit its confirmation as to whether a video CPU has completed its previous processing. Also, in this display control unit (3), an updating processing of color specification in display data can be performed at increased speeds, only desired dots on a display screen (5) can be logically operated, a form or an object having a solid body on a transparent background within a source area can be transferred at higher speeds, and an expansion memory (111) can be used as a "kanji (chinese character)" ROM (pattern memory) or the like.

IPC 1-7

G09G 1/16; G06F 3/153

IPC 8 full level

G09G 1/16 (2006.01); **G09G 5/00** (2006.01); **G09G 5/39** (2006.01); **G09G 5/393** (2006.01); **G09G 5/24** (2006.01); **G09G 5/395** (2006.01)

CPC (source: EP)

G09G 5/001 (2013.01); **G09G 5/246** (2013.01); **G09G 5/393** (2013.01); **G09G 5/395** (2013.01); **G09G 2360/127** (2013.01)

Citation (search report)

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