

Title (en)  
CIRCUIT ARRANGEMENT FOR A DANGER-INDICATING INSTALLATION

Publication  
**EP 0158217 B1 19880824 (DE)**

Application  
**EP 85103600 A 19850326**

Priority  
DE 3411129 A 19840326

Abstract (en)  
[origin: EP0158217A1] 1. Circuit arrangement for a danger-indicating installation with several indicators (4, 5), which are connected across a two-wire indicating line (2, 3) to a central means having evaluation electronics (20) and which associates the signals supplied by indicators (4, 5) with clearly defined states on the indicators, with a signal level applied to the indicators by the evaluation electronics (20) via indicating line (2, 3) and which is cyclicly keyed, with in each case one counter (43) providing in the indicators (4, 5) and which counts the logic states corresponding to this signal level keying and in the case of identity of the count with an address value in each case preset on each indicator supplies a signal characterizing its state across indicating line (2, 3) to evaluation electronics (20), all the counters (43) being resettable by a resetting signal, characterized in that the resetting signal for the counters (43) of indicators (4, 5) is in the form of a further signal level keying and that the signal characterizing the state, e.g. alarm of fault, of the individual indicator (4, 5) is supplied as a clearly defined, but different signal level to evaluation electronics (20) by the selected indicator (4, 5) within the time interval up to the selection of the next indicator.

IPC 1-7  
**G08B 25/00**; **G08B 26/00**

IPC 8 full level  
**G08B 25/00** (2006.01); **G08B 25/04** (2006.01); **G08B 26/00** (2006.01)

CPC (source: EP)  
**G08B 25/04** (2013.01); **G08B 26/002** (2013.01)

Cited by  
DE102015223207A1; FR2756650A1; EP0248298A1; US4757303A; EP0717386A1; FR2728373A1

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DOCDB simple family (publication)  
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