

Title (en)  
ARRANGEMENT FOR THE EXCITATION OF RELAYS

Publication  
**EP 0164014 B1 19880824 (DE)**

Application  
**EP 85106226 A 19850521**

Priority  
DE 3420057 A 19840529

Abstract (en)  
[origin: EP0164014A1] 1. An arrangement for energising relays, in particular in signal arrays in racks of digital signal and carrier frequency systems, characterised in that a relay (1), a parallel arrangement composed of a first resistor (2) and a first switch (3), and a second switch (5) are arranged in a first series circuit which is connected to an operating voltage (UB ), that a comparator (6) is provided which is connected to the operating voltage (UB ) and which emits a logic 0 when an upper value of the operating voltage (UB ) is connected and which emits a logic 1 when a lower value is connected, that a monostable trigger circuit (7) is provided at whose input (8) an energising signal can be connected, that a first OR-gate (9) is provided, whose first input is connected to the output of the comparator (6), whose second input is connected to the output of the monostable trigger circuit (7), and whose output is connected via a first switch drive unit (10) to a control input of the first switch (3), that a second OR-gate (11) is provided, whose first input is connected to the output of the monostable trigger circuit (7) and whose second input is connected to the input (8) thereof and whose output is connected via a second switch drive unit (12) to a control input of the second switch (5), and that the value of the first resistor (2) is such that at an upper value of the operating voltage the holding voltage of the relay (1) is not undershot (Fig. 1).

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IPC 8 full level  
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CPC (source: EP)  
**H01H 47/10** (2013.01); **H01H 47/22** (2013.01)

Cited by  
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