

Title (en)

Activity trace collection mechanism for peripheral devices in computer systems.

Title (de)

Mechanismus zur Tätigkeitsspurenentnahme für Peripheriegeräte in Rechnersystemen.

Title (fr)

Mécanisme de prélèvement de trace d'activité pour dispositifs périphériques dans des systèmes de calcul.

Publication

EP 0164673 A2 19851218 (EN)

Application

EP 85106823 A 19850603

Priority

US 62098084 A 19840615

Abstract (en)

A trace collection mechanism for recording accesses to a peripheral device (12) having an I/O cache memory (20), comprises a clock (30) and control means (16). At each access of the memory, the beginning and ending times of the access are stored in a part (28) of the cache memory, together with parameters of the access, such as address and length of accessed data and the type of access. A set of collected data can be analyzed for the operation of both the peripheral device and the central processing program.

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IPC 8 full level

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CPC (source: EP)

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