

Title (en)
PHASE ANALOG ENCODING SYSTEM WITH COMPENSATION

Publication
EP 0165304 B1 19920401 (EN)

Application
EP 85900395 A 19841210

Priority
US 56065883 A 19831212

Abstract (en)
[origin: WO8502702A1] A phase analog encoding system with compensation for the phase shift error inherent in a resolver position transducer (2). The inherent phase shift error in a resolver position transducer is compensated in a time multiplexed fashion by periodically applying a known reference signal (25) or (26) to the resolver and measuring the electrical phase shift across only the resolver. The newly measured value of the inherent electrical phase shift is compared with the last measured value of the inherent electrical phase shift and any deviation is calculated. This deviation is used to determine the appropriate compensation value for the inherent electrical phase shift. The compensation value is subtracted from the resolver phase shift during normal operation resulting in an output signal which is independent of the inherent electrical phase shift error in the resolver position transducer.

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