

Title (en)
SPRITE COLLISION DETECTOR

Publication
EP 0165665 A3 19890222 (EN)

Application
EP 85302464 A 19850409

Priority
US 60068884 A 19840416

Abstract (en)
[origin: EP0165665A2] A sprite collision detector includes the combination (1) of a video processor including a color palette for processing of data, a memory (31) for storing of data to be processed, a monitor (33) and (35) for displaying of the processed data, a processor (30) for controlling the transfer of data from the memory (30) to the monitor (33) and (35). The video processor (1) includes a control logic (65) for controlling the transfer of data and instructions between the processor (30) and the video processor (1); a memory control logic (67) for controlling, in response to the instructions from the processor (1), the transfer of data from the memory (31); sprite processor (10) for converting data to color data and to provide the color data for images displayed by the monitor (33) and (35); register (63) for arranging data from the memory (31) in a predetermined order and to apply the data according to the prearranged order to the sprite processor (10); and video output logic (57) for converting the color data to a video signal to which the monitor (33) and (35) will respond to provide a display of a predetermined pattern and of a predetermined color.

IPC 1-7
G09G 1/16; **G09G 1/28**

IPC 8 full level
G06F 3/153 (2006.01); **G06T 13/00** (2011.01); **G06T 13/80** (2011.01); **G09G 5/00** (2006.01); **G09G 5/06** (2006.01); **G09G 5/38** (2006.01); **G09G 5/42** (2006.01)

CPC (source: EP)
G09G 5/06 (2013.01); **G09G 5/42** (2013.01)

Citation (search report)

- [YD] US 4296476 A 19811020 - MAYER STEVEN T, et al
- [YD] US 4243984 A 19810106 - ACKLEY DAVID A, et al
- [Y] EP 0061213 A1 19820929 - PHILIPS NV [NL]
- [AD] US 4119955 A 19781010 - NICHOLS III ANDREW J
- [A] GB 2055279 A 19810225 - NAMCO LTD
- [A] TWENTY-FIRST IEEE COMPUTER SOCIETY INTERNATIONAL CONFERENCE, Washington, D.C., 23rd-25th September 1980, pages 219-223, IEEE, New York, US; K. GUTTAG: "The TMS 9918 video display processor: a brief overview"

Cited by
EP0318517B1

Designated contracting state (EPC)
DE FR GB

DOCDB simple family (publication)
EP 0165665 A2 19851227; **EP 0165665 A3 19890222**; JP S6122395 A 19860130

DOCDB simple family (application)
EP 85302464 A 19850409; JP 8120785 A 19850416