

Title (en)
PROCESS FOR MANUFACTURING PRINTED CIRCUIT BOARDS

Publication
EP 0166327 B1 19881221 (DE)

Application
EP 85107381 A 19850614

Priority
DE 3423457 A 19840626

Abstract (en)
[origin: EP0166327A2] 1. Process for the manufacture of printed circuits by the wet-chemical metallizing of baseboards with the aid of organometallic activators, characterized in that a) the baseboards are activated, without prior etching, with a solution of 0.01-10 g/l of a complex compound of Pd, Au, Pt or Ag with a conjugated diene, alpha, beta unsaturated ketone, 4-cyclohexene-1,2-dicarboxylic anhydride or 4-cyclohexene-1,2-dicarboxylic imide, b) an electronically conductive metal coating with a layer thickness of 0.05-10 mu m and a stability to separation of at least 25 N/25 mm according to DIN 53 494 is applied to the boards activated in this way, by a wet chemical-electroless method or a combined wet chemical-electroless/electroplating method, and c) the baseboards metallized in this way, i. e. the so-called semi-finished products, are further processed in the customary manner to produce printed circuits.

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C23C 18/28; **H05K 3/18**

IPC 8 full level
H05K 3/18 (2006.01); **C23C 18/16** (2006.01); **C23C 18/26** (2006.01); **C23C 18/28** (2006.01); **C23C 18/30** (2006.01); **C23C 18/31** (2006.01); **C23C 18/40** (2006.01); **C23C 18/44** (2006.01); **C23C 18/50** (2006.01); **C23C 18/52** (2006.01); **C25D 7/00** (2006.01)

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